

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	56039	power near5 (processor microprocessor cpu mpu)	US- PGPUB; USPAT	2005/05/13 16:15
2	BRS	L2	31754	(stop\$4 sleep) same clock	US- PGPUB; USPAT	2005/05/13 16:16
3	BRS	L3	9424	(low\$2 near3 power) same clock	US- PGPUB; USPAT	2005/05/13 16:17
4	BRS	L4	1251	1 same 2	US- PGPUB; USPAT	2005/05/13 16:17
5	BRS	L5	1237	1 same 3	US- PGPUB; USPAT	2005/05/13 16:17
6	BRS	L6	1922	4 or 5	US- PGPUB; USPAT	2005/05/13 16:19
7	BRS	L7	452018	(input\$4 receiv\$3) near3 data	US- PGPUB; USPAT	2005/05/13 16:21
8	BRS	L8	1086	6 and 7	US- PGPUB; USPAT	2005/05/13 16:27
9	BRS	L9	264725	(start\$3 wak\$3 initiat\$3) near5 (microprocessor process\$3)	US- PGPUB; USPAT	2005/05/13 16:29
10	BRS	L10	574	8 and 9	US- PGPUB; USPAT	2005/05/13 16:29
11	BRS	L11	70189	transition\$3 near5 (state mode)	US- PGPUB; USPAT	2005/05/13 16:30
12	BRS	L12	192	10 and 11	US- PGPUB; USPAT	2005/05/13 16:30
13	BRS	L13	2508	713/300-320.ccls.	US- PGPUB; USPAT	2005/05/13 16:31
14	BRS	L14	1464	713/322-324.ccls.	US- PGPUB; USPAT	2005/05/13 16:31
15	BRS	L15	3443	13 or 14	US- PGPUB; USPAT	2005/05/13 16:32

	Type	L #	Hits	Search Text	DBs	Time Stamp
16	BRS	L16	87	12 and 15	US- PGPUB; USPAT	2005/05/13 16:32